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Sheet 2 of 2

	Complete if Known
Application Number	10/684,457
Filing Date	October 15, 2003
First Named Inventor	Chul-Sung KIM, et al
Art Unit	
Examiner Name	
Attorney Docket Number	239/162 DIV.1

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
TTN		WOLF, et al., Silicon Processing for the VLSI Era, Vol 1, Process Technology, Lattice Press, p.201	
TTN		LO, et al, MOS Characteristics of Fluorinated ..., IEEE Electron Device Ltrs, 11(11):511-513, (1990)	
TTN		WRIGHT, et al., Hot-Electron Immunity of SiO2 Dielectrics with Fluorine.., IEEE, pp. 347-348, (1989)	
TTN		HOOK, et al., Correlation Between Fixed Positive Charge and Hot-Electron., IEEE, pp. 471-473, (1997)	

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